## REMARKS

In view of the above amendments and the following remarks, reconsideration and further examination are requested.

By this amendment, claims 13-18 have been canceled and claims 19-30 have been added. Thus, claims 19-30 remain pending. Support for the new claim recitations can be found at least at: column 60, lines3-6; Fig. 63b; and column 64, lines 2-7. If the Examiner requires further supporting passages, she is invited to contact the undersigned by telephone.

In light of the Examiner's requirement, formal drawings for the present application are filed herewith.

Claims 13-18 were rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claim 1 of U.S. Patent No. 5,600,672. It is submitted that this rejection is improper and inapplicable to claims 19-30. The present application is a reissue application of U.S. Patent No. 5,600,672. Further, the original patent was surrendered to the PTO on July 18, 2001 in the parent application of the present application. The Serial No. of the parent application is 09/244,037. Moreover, as is the case in the present application, in the parent application claim 1 of the original patent 5,600,672 has been canceled, and original claim 1 is not present in any of the currently pending reissue applications of U.S. Patent No. 5,600,672.

Claims 13-18 were rejected under the judicially-created doctrine of obviousness-type double patenting as being unpatentable over claims 2 and 5 of U.S. Patent No. 6,256,357, and over claims 13-18 of co-pending applications nos. 09/672,947, 09/668,068, and 09/666,012 in view of Bryan. Further, claims 15-18 were rejected under the judicially-created doctrine of obviousness-type double patenting as being unpatentable over claims 13-18 of co-pending applications nos. 09/669,916, 09/667,525, 09/666,012, and 09/672,948. These rejections are not applicable to new claims 19-30.

Independent claims 19 and 20 each recite a DC offset generator operable to add a DC offset to the n-level mapped signal to produce a VSB modulated signal including a carrier produced by the DC offset. Likewise, independent method claims 27 and 28 recite adding a DC offset to the n-level mapped signal to produce a VSB modulated signal including a carrier produced by the DC offset. Independent claims 21 and 29 each recite that the VSB modulated signal includes a carrier produced

by a DC offset. None of the co-pending applications or the patent applied in the obviousness-type double patenting rejections includes any recitation to such a DC offset feature. Accordingly, the obviousness-type double patenting rejections set forth in the Office Action are not applicable to new claims 19-30.

In view of the above amendments and remarks, it is submitted that the present application is in condition for allowance. The Examiner is invited to contact the undersigned attorney by telephone to resolve any remaining issues.

Respectfully submitted,

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By

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